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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
09/851,504	05/08/2001	Theodore F. Vaida	01-036	2218
7590	06/28/2006		EXAMINER	
Wendy Taylor LSI Logic Corporation 1551 McCarthy Boulevard Mail Stop D-106 Milpitas, CA 95035				DUONG, DUC T
		ART UNIT	PAPER NUMBER	
		2616		
DATE MAILED: 06/28/2006				

Please find below and/or attached an Office communication concerning this application or proceeding.

Office Action Summary	Application No.	Applicant(s)
	09/851,504	VAIDA ET AL.
	Examiner	Art Unit
	Duc T. Duong	2616

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --

Period for Reply

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If the period for reply specified above is less than thirty (30) days, a reply within the statutory minimum of thirty (30) days will be considered timely.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

Status

1) Responsive to communication(s) filed on 02 May 2006.
 2a) This action is **FINAL**. 2b) This action is non-final.
 3) Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

Disposition of Claims

4) Claim(s) 1-7,9-17 and 19-42 is/are pending in the application.
 4a) Of the above claim(s) _____ is/are withdrawn from consideration.
 5) Claim(s) 21-42 is/are allowed.
 6) Claim(s) 1-7,9-17,19 and 20 is/are rejected.
 7) Claim(s) _____ is/are objected to.
 8) Claim(s) _____ are subject to restriction and/or election requirement.

Application Papers

9) The specification is objected to by the Examiner.
 10) The drawing(s) filed on _____ is/are: a) accepted or b) objected to by the Examiner.
 Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).
 Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).
 11) The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.

Priority under 35 U.S.C. § 119

12) Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
 a) All b) Some * c) None of:
 1. Certified copies of the priority documents have been received.
 2. Certified copies of the priority documents have been received in Application No. _____.
 3. Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).

* See the attached detailed Office action for a list of the certified copies not received.

Attachment(s)

1) <input checked="" type="checkbox"/> Notice of References Cited (PTO-892)	4) <input type="checkbox"/> Interview Summary (PTO-413)
2) <input type="checkbox"/> Notice of Draftsperson's Patent Drawing Review (PTO-948)	Paper No(s)/Mail Date. _____.
3) <input type="checkbox"/> Information Disclosure Statement(s) (PTO-1449 or PTO/SB/08) Paper No(s)/Mail Date _____.	5) <input type="checkbox"/> Notice of Informal Patent Application (PTO-152)
	6) <input type="checkbox"/> Other: _____.

DETAILED ACTION

Response to Amendment

1. Applicant's request for reconsideration of the finality of the rejection of the last Office action is persuasive and, therefore, the finality of that action is withdrawn.

Claim Rejections - 35 USC § 103

2. The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:

(a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negated by the manner in which the invention was made.

3. Claims 1-7, 9-17, 19, and 20 are rejected under 35 U.S.C. 103(a) as being unpatentable over Pandya et al (US Patent 6,792,502 B1) in view of Parviainen (US Patent 6,959,316 B1).

Regarding to claims 1 and 11, Pandya discloses a programmable network application specific integrated circuit 300 (fig. 7 col. 11 lines 14-18), comprising a media access controller 315 (col. 11 lines 9-13) configured to transmit and receive network data via a physical interface device 362/364 (col. 11 lines 2-9); a programmable logic core 310 having a plurality arithmetic logic units 460 (fig. 8 col. 12 lines 16-26), said programmable logic core 310 configured to interface with said media access controller 315 and implement least one application level function capable of generating meta-data (col. 11 lines 31-39).

Pandya fails to teach the arithmetic logic units are dynamically configurable.

However, Parvianen discloses a data processor 10 having a plurality of arithmetic logic units ALUs that are dynamically configurable (fig. 6 col. 7 lines 36-40).

Thus, it would have been obvious to a person of ordinary skill in the art to employ dynamically configurable ALUs in Pandya's system to provide flexibility in the operations of ALUs.

Regarding to claims 2 and 12, Pandya discloses the programmable logic core 310 may be programmed while said least one application level function executing (col. 12 lines 32-38).

Regarding to claims 3, and 13, Pandya discloses the programmable network application specific integrated circuit 300 comprising a data interconnect subsystem 370 configured to transmit and receive said network data from said MP-block (fig. 7 col. 11 lines 33-36) and a function master subsystem 350 configured to receive said meta-data from said MP-block and dynamically program said programmable logic units 310 (fig. 7 col. 11 lines 52-55).

Regarding to claims 4 and 14, Pandya discloses the data interconnect system 370 is further configured to transmit and receive said network data from a host system 390 (fig. 7 col. 11 lines 36-39).

Regarding to claims 5 and 15, Pandya discloses the function master subsystem 350 is configured to transmit said meta-data to a host system 390 and capable of receiving programming instructions from said host system 390 (fig. 7 col. 11 lines 55-61).

Regarding to claims 6 and 16, Pandya discloses the function master subsystem 350 is capable of programming said programmable logic core 310 based upon said meta-data (fig. 7 col. 11 lines 55-61).

Regarding to claim 7 and 17, Pandya discloses the function master subsystem 350 is capable of programming said programming logic core based upon said network data (fig. 7 col. 11 lines 55-61).

Regarding to claims 9 and 19, Pandya discloses at least one application level function is a content based addressing (col. 3 lines 10-27).

Regarding to claims 10 and 20, Pandya discloses the programmable logic core 310 includes a management interface (implicitly shown) configured to control and manage said media access controller 315 (fig. 7 col. 11 lines 18-30).

Allowable Subject Matter

4. Claims 21-42 are allowed.

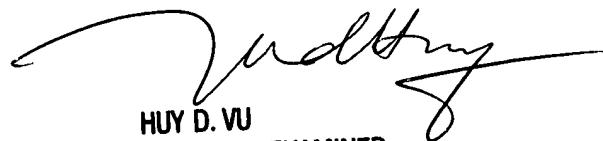
Conclusion

5. Any inquiry concerning this communication or earlier communications from the examiner should be directed to Duc T. Duong whose telephone number is 571-272-3122. The examiner can normally be reached on M-F (9:00 AM-6:00 PM).

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Huy D. Vu can be reached on 571-272-3155. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free). If you would like assistance from a USPTO Customer Service Representative or access to the automated information system, call 800-786-9199 (IN USA OR CANADA) or 571-272-1000.

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DD



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